UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,292,622 B2
APPLICATION NO. : 10/680492
DATED : November 6, 2007
INVENTOR(S) : John W. McCorkle

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 24, Line 19, Claim No. 20:

Change "main correlated signal and producing a digits bit value;" to --main correlated signal and producing a digital bit value;--

In Column 24, Line 31, Claim No. 21:

Change "a main path integrator circuit fOr integrating the main" to --a main path integrator circuit for integrating the main--

In Column 24, Line 37, Claim No. 22:

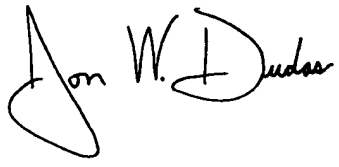
Change "wherein the main path integrator circuit farther" to --wherein the main path integrator circuit further--

In Column 26, Line 8, Claim No. 26:

Change "weighted wavelets, respectively," to --weighted wavelets, respectively;--

Signed and Sealed this

Twenty-ninth Day of July, 2008



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Director of the United States Patent and Trademark Office